



# United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/629,191	07/28/2003	Curtis E. Stevens	155607-0459	5682
7590 05/17/2005			EXAMINER	
Loren H. McRoss			ВАЕ, ЈІ Н	
Phoenix Technologies Ltd. 915 Murphy Ranch Road			ART UNIT T	PAPER NUMBER
Milpitas, CA 95035			2115	
			DATE MAILED: 05/17/2005	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
	10/629,191	STEVENS, CURTIS E.			
Office Action Summary	Examiner	Art Unit			
	Ji H. Bae	2115			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION  - Extensions of time may be available under the provisions of 37 CFR 1. after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a replication of the period for reply specified above, the maximum statutory period Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	. 136(a). In no event, however, may a reply be timply within the statutory minimum of thirty (30) day d will apply and will expire SIX (6) MONTHS from te, cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).			
Status	•				
1) ☐ Responsive to communication(s) filed on 28.  2a) ☐ This action is <b>FINAL</b> . 2b) ☐ This action is <b>FINAL</b> . 2b) ☐ This action is application is in condition for allowed closed in accordance with the practice under	is action is non-final. ance except for formal matters, pro				
Disposition of Claims					
4) Claim(s) 18-67 is/are pending in the application 4a) Of the above claim(s) is/are withdres 5) Claim(s) is/are allowed. 6) Claim(s) 18-67 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/	awn from consideration.				
Application Papers					
9) The specification is objected to by the Examin 10) The drawing(s) filed on is/are: a) ac Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the E	cepted or b) objected to by the le drawing(s) be held in abeyance. Section is required if the drawing(s) is objection.	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).			
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreig a) All b) Some * c) None of:  1. Certified copies of the priority documer 2. Certified copies of the priority documer 3. Copies of the certified copies of the pri application from the International Burea * See the attached detailed Office action for a list	nts have been received.  Its have been received in Applicationity documents have been received au (PCT Rule 17.2(a)).	on No ed in this National Stage			
Attachment(s)  1) ☒ Notice of References Cited (PTO-892)  2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 11-17-2003	4) Interview Summary Paper No(s)/Mail Do 5) Notice of Informal F 6) Other:				

Art Unit: 2115

#### **DETAILED ACTION**

### **Priority**

Applicant has not complied with one or more conditions for receiving the benefit of an earlier filing date under 35 U.S.C. 120 as follows:

An application in which the benefits of an earlier application are desired must contain a specific reference to the prior application(s) in the first sentence(s) of the specification or in an application data sheet by identifying the prior application by application number (37 CFR 1.78(a)(2) and (a)(5)). If the prior application is a non-provisional application, the specific reference must also include the relationship (i.e., continuation, divisional, or continuation-in-part) between the applications except when the reference is to a prior application of a CPA assigned the same application number.

## **Double Patenting**

The nonstatutory double patenting rejection is based on a judicially created doctrine grounded in public policy (a policy reflected in the statute) so as to prevent the unjustified or improper timewise extension of the "right to exclude" granted by a patent and to prevent possible harassment by multiple assignees. See *In re Goodman*, 11 F.3d 1046, 29 USPQ2d 2010 (Fed. Cir. 1993); *In re Longi*, 759 F.2d 887, 225 USPQ 645 (Fed. Cir. 1985); *In re Van Ornum*, 686 F.2d 937, 214 USPQ 761 (CCPA 1982); *In re Vogel*, 422 F.2d 438, 164 USPQ 619 (CCPA 1970);and, *In re Thorington*, 418 F.2d 528, 163 USPQ 644 (CCPA 1969).

A timely filed terminal disclaimer in compliance with 37 CFR 1.321(c) may be used to overcome an actual or provisional rejection based on a nonstatutory double patenting ground provided the conflicting application or patent is shown to be commonly owned with this application. See 37 CFR 1.130(b).

Effective January 1, 1994, a registered attorney or agent of record may sign a terminal disclaimer. A terminal disclaimer signed by the assignee must fully comply with 37 CFR 3.73(b).

Claims 18-67 are rejected under the judicially created doctrine of obviousness-type double patenting as being unpatentable over claims 1-17 of U.S. Patent No. 6,633,976 B1 [Stevens]. Although the conflicting claims are not identical, they are not patentably distinct from each other because applicant's invention comprises embodiments that implement the same

Application/Control Number: 10/629,191

Art Unit: 2115

steps as the method taught by Stevens. In particular, it is noted that applicant recites a computer program product for processing BIOS modules, a system for processing BIOS modules, and a method for processing BIOS modules. It would have been obvious to one of ordinary skill in the art to implement the method of Stevens in any number of possible embodiments, including a computer program product, a computer system, a process, or a method.

Claims 18-30 and 31-43 of the present application correspond to claims 1-13, respectively, of Stevens.

Claims 44-45 and 56-57 of the present application correspond to claims 14-15, respectively, of Stevens.

Claims 46 and 58 of the present application correspond to claim 4 of Stevens.

Claims 47-54 and 59-66 of the present application correspond to claims 5-12 and 16 of Stevens.

Claims 55 and 67 of present application correspond to claim 17 of Stevens.

#### Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Cheston et al., U.S. Patent No. 6,725,178 B2, teaches the use of hidden partitions on a storage device for storing BIOS modules.

Frank, Jr. et al., U.S Patent No. 6,546,489 B1, discloses a disk drive that allows for booting of a computer system from a protected area of the drive.

Application/Control Number: 10/629,191 Page 4

Art Unit: 2115

Bealkowski et al., U.S. Patent No. 5,355,489, discloses an apparatus for loading a BIOS module from a direct access storage device. A first portion of the BIOS is loaded from a ROM, which executes a master boot record to load the remainder of the BIOS of the storage device.

Dewa et al., U.S. Patent No. 5,522,076, discloses a method for re-writing a BIOS-ROM.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ji H. Bae whose telephone number is 571-272-7181. The examiner can normally be reached on Monday-Friday, 10 am to 6:30 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Thomas Lee can be reached on 571-272-3667. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Ji H. Bae Patent Examiner Art Unit 2115 ji.bae@uspto.gov

THOMAS LEE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100